

Abstract Details

Title: Error Control Capability of Orthogonal Code Convolution by Means of FPGA Implementation

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Abstract: Information and communication technology has brought enormous changes to our life. With the increase of data transmission and hence source of noise and interference, engineers have been struggling with the demand for more efficient and reliable techniques for detecting and correcting errors in received data. Although several techniques and approaches have been proposed and applied in the last decade, data reliability in transmission is still a problem. In this paper, the orthogonal codes has been developed and realized by mean of field programmable gate array (FPGA). Simultaneous result shows that the proposed technique improves the detection capability of the orthogonal code.

Keywords: Error detection and correction, FPGA, Orthogonal Code Antipodal Code.